

DVR1

Precision Temperature-Regulated DC Voltage Reference



Triple Reference Output

Voltages of 10V, 1V and 0,1V or triple 10V (3 Outputs);

New version with reduced aging drift and Calibration uncertainty

Temperature Stabilized

Includes internal oven for low temperature drift

Cost Efficient Design

High Precision

Calibrated against low tolerance standards

Low Drift

Based on low drift buried zener diodes

Temperature Sensor

Integral PT100 sensor for temperature surveillance

Options

Versions e.g. with binding posts on request

Input Power Conditioning

Power input with reverse polarity protection, voltage regulation and EMI filtering

Output Protection

Short circuit proof output
Output EMI filtering

The DVR1 is a very cost efficient, yet precision voltage reference assembly, capable of generating three precise and low-drift, buffered reference voltages of 10V (single voltage version) or 10V, 1V and 0,1V (triple voltage version). It incorporates a regulated oven to stabilize the output voltages, thus limiting the impact of ambient temperature fluctuations. The DVR1 is an OEM product to be incorporated in equipment where precise reference voltages are needed, such as e.g. in industry electronics, physics applications or for calibration purposes.

The use of buried zener diodes in hermetic package (LT1021) in combination with other low drift components and a precision temperature control circuitry is the prerequisite of a precision voltage standards. The design, which does not require continuous operation to achieve stable output voltages, results in a very attractive price/performance ratio. Other cost efficient, yet temperature compensated standards often use thermistors to compensate thermal drifts and non-hermetic reference chips, resulting in sub-optimal temperature compensation and higher aging related drifts. This, amongst other reasons, is because exact trimming of the thermistor to each specific reference temperature gradient is essentially impossible for production units. Non-hermetic reference chips show higher aging drift rates due to environmental impacts. Therefore this reference assembly uses an internal precision heater working on component level and a hermetic voltage reference chip. A PT100 temperature sensor enables monitoring of the reference device temperature.

Every product is factory calibrated, traceable to national standards after a minimum of 500 hours of pre-aging for the references and an additional 100 hours for the complete unit. The calibration includes the adjustment of the 10V output and the precision measurement of the 1V and 0.1V output (for the tripple voltage version). The single output version supports 3 outputs with 10V each (on special request only). The first output is adjusted to exactly 10V by use of a trimmer, the remaining two are calibrated (measured). The trimmer is accessible by the user from outside to support re-adjustment. The unit is accompanied by factory calibration data including specification of the absolute tolerances. Its high off state stability also makes it ideal as a voltage travel standard.

Versions can be made available, e.g. with 10V, 2V and 0.2V output voltages or with four brass or copper-tellurium binding posts as output terminals (depending on the thermal EMF requirements). For further details see also www.ab-precision.com.

DVR1

Datasheet

IMPORTANT NOTE: Power GND and Output GND **are NOT** on the same potential, **do NOT connect** together

All values are typical specifications at 23°C, K=2, unless otherwise specified:

Parameter	Condition	Specification
Ambient Conditions	Operation, per specification Operation, no damage Storage, transport	18 - 28°C 10 - 35°C 5 - 45°C
Output 1 Voltage 10V	Absolute calibration uncertainty 30 day drift *) ₆ 1 year drift *) ₆	< 1,5 ppm max. *) ₁ < 1,5 ppm < 5 ppm
Output 2 Voltage 1V *) ₂	Absolute calibration uncertainty 180 day drift *) ₆	< 7 ppm max. *) ₁ < 10 ppm
Output 3 Voltage 0.1V *) ₂	Absolute calibration uncertainty 180 day drift *) ₆	< 10 ppm max. *) ₁ < 20 ppm
Thermal Drift	10V output, T=18-28 °C *) ₆	0.1 ppm/K typ. *) ₄ < 0.25 ppm/K max. *) ₄
Output Noise Voltage	10V output, 0.1-10Hz	< 1 ppm peak-peak
PT100 temperature tolerance	4 wire measurement	< 0.5K
Warmup Time	10V output	30 min. to $\partial U < 1$ ppm of nominal output voltage
Output Impedance	all outputs	< 2 Ohms
DC Supply Voltage *) ₅	Specification / no damage Peak startup current Average current (heated up)	17.5-18.5V / 17.0-24.0V < 150mA *) ₇ < 50mA
Output Current	Short circuit, any output Recommended output current	Indefinite *) ₃ <1 μ A
I/O Connectors	All standard versions (mating Connector supplied)	9 pin D-Sub female; Four 4mm posts optionally
Weight	All standard versions	Appr. 150g

IMPORTANT Notes:

*)₁: Relative to National Standards (PTB) at time of calibration; Output 1 is adjusted to a nominal 10V by trimmer on item top side (see Marking); calibration is lost on all channels if trimmer is adjusted by user. Use non-conductive screw driver only!

*)₂: Only tripple voltage versions support this output voltage

*)₃: One 10V output ONLY at a time, for 2 or 3 damage will occur! After removal of short circuit, allow 30 min temperature stabilisation.

*)₄: Averaged over a 10K temperature range, total is <2.5ppm over a 10K temperature range

*)₅: We recommend to use a linear regulated, low noise supply. Avoid switchmode regulators to minimize noise and wrong voltage readings.

*)₆: To calculate total output uncertainties, add calibration uncertainty, aging- and temperature-drift. Aging spec. related to 99% off-state.

Note that for continuous operation the drift rate is typically 15ppm/ $\sqrt{(t/1000h)}$, non cumulative. Thermal EMF voltages may add.

*)₇: Although an internal fuse is installed, use external fuse 500mA slow blow to limit supply current

- For specified values, ambient temperature gradient shall be < 1K/h and item shall be operated in horizontal position

- 'K' denotes temperature changes, relative to absolute values indicated in °C. E.g. a 1K increase equates to a change from x °C to x+1 °C

DVR1

Datasheet

Circuit Description:

The DRV1 consists of the following functional blocks to achieve the performance specified:

- Input reverse voltage protection and power filtering, ESD protection
- Dual, separate, voltage regulator stage for reference circuitry and heater circuitry
- LT1021 buried zener voltage reference, hermetic, for low aging drift, voltage adjustable
- Precision voltage divider stages (3-voltage version only) with ultra low temp. coefficient
- Output amplifier (per output) with output protection (ESD, short circuit, EMI-filtering)
- Reference chip noise filter
- Precision PI heater control circuitry for reference (and related circuitry)
- PT100 temperature sensor for internal reference temperature monitoring
- Metal case for improved ESD/EMI shielding

Intended Use, Operation Recommendations and Precautions:

This reference assembly is a precision device and special care must be taken when operating it to achieve optimum performance. It is not intended as a self-standing device but as an assembly to be used embedded in an electronic equipment. Handle carefully and ensure a temperature stabilized environment as specified. During operation avoid any direct air drafts, device movement, direct infrared radiation and other heat sources in proximity to the unit. Take the usual precautions to minimize thermal EMF voltages when connecting cables. Note that the brass binding post output version has increased EMF. Avoid temperature extremes whenever possible (also in storage). Do not exceed specified storage and operating conditions. Use short, twisted shielded cables for each output. Connect shield to GND. Avoid ground loops. Switch item off if not in use to minimize aging whenever possible. See aging rate data.

Please note: The power input negative pin and the output GND pins are **NOT** on the same potential and **MUST NOT** be connected together. Unit generates its own output reference GND level, to be used as the output reference potential (pins 1 and 6 of output connector).

D-Sub I/O Connector (9 pin female):

- Pin 5, 9: PT100 (use 4-wire connection at connector terminals)
- Pin 8: Positive DC supply input, 18V nominal, protect with external fuse 500mA/slow
- Pin 4: Negative DC supply input (**DO NOT** connect to chassis or GND, Pins 1 and 6!)
- Pin 1, 6: Output GND (connected to chassis, use signal also for cable shield connection)
- Pin 3: Output 1 (10V; adjusted to nominal, see page 2)
- Pin 7: Output 2 (1V; respectively 10V with single output version)
- Pin 2: Output 3 (0.1V; respectively 10V with single output version)

Note: The DVR1-x-A-N assembly ships with a mating D-Sub connector (with solder cups) and a D-Sub shell

Order Number	Description
DVR1-1-A-N	Precision DC Voltage Reference Assembly, 10V output (3 separate 10V outputs); NOT ROHS compliant; This version is on special request only
DVR1-3-A-N	Precision DC Voltage Reference Assembly, three output voltages, 10V, 1V, 0.1V output; ; NOT ROHS compliant

DVR1

Datasheet

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